



# Rapid Prototyping of Digital Systems

*James O. Hamblen, Michael D. Furman*

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*Rapid Prototyping of Digital Systems* provides an exciting and challenging laboratory component for undergraduate digital logic and computer design courses. The more advanced topics and exercises also make this text useful for upper level courses in digital logic or programmable logic. Design engineers working in industry will want to consider this text for a rapid introduction to PPLD technology and logic synthesis using commercial CAD tools.

*Rapid Prototyping of Digital Systems* includes two tutorials on the Altera CAD tool environment, an overview of programmable logic, and a design library with several easy-to-use input and output functions. These features were developed to help students get started quickly. Early design examples use schematic capture and library components. VHDL is used for more complex designs after a short introduction to VHDL-based synthesis.

The approach used in this text reflects contemporary practice in industry more accurately than the more traditional TTL protoboard-based laboratory courses. Designs containing up to twenty thousand gates are possible with the Altera Student Version CAD tools and the UP 1 board.

*Rapid Prototyping of Digital Systems* contains a number of interesting and challenging laboratory projects involving serial communications, state machines with video output, video games and graphics, simple computers, keyboard and mouse interfaces, robotics, and a RISC processor core. These projects were all developed on the student version of the Altera CAD tools and can be implemented on the Altera UP 1 board.

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